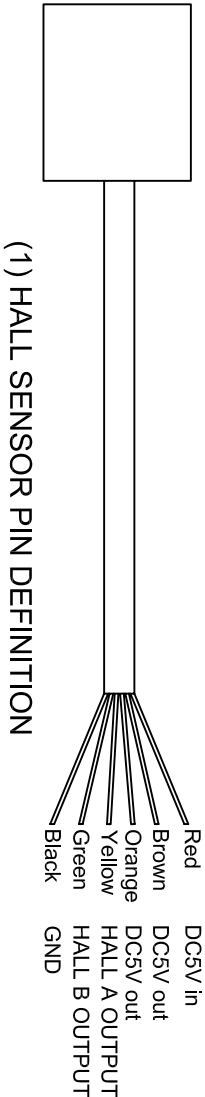
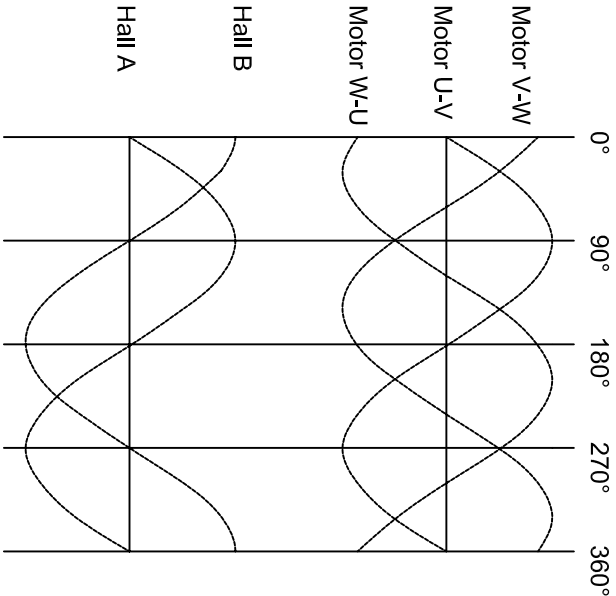


ANALOG HALL SENSOR OF HIWIN LINEAR MOTOR



(1) HALL SENSOR PIN DEFINITION



(2) HALL SENSOR OUTPUT SIGNAL

A/B signal voltage: 1V \pm 0.2Vp-p

A/B amplitude difference: $\leq \pm 0.1$ V (VAp-p - VBp-p)

A/B phase difference: 90 \pm 10 degree

Power supply: DC 5V \pm 5%

Motor U-V phase and hall sensor A phase difference: $\leq \pm 10$ degree

Output offset voltage of LMS: 2.5V (LMAHSA)

Output offset voltage of LMC: 1.5V (LMAHCA)

BREAK SHARP EDGE UNLESS OTHER SPECIFIED										
TYPE/ CUSTOMER			Analog Hall Sensor			SCALE		1:X		
NAME						DATE		JUL.23.04'		
MATERIAL						DRN		洪佳君		
MODEL			DWG.NO		SHEET		CHK			
UP			6		OVER		APPD			
			30		1200					
TO			6		1200					
			30		2400					
			120		2400					
			120		2400					
			300		2400					
			600		2400					
			1200		2400					
			2400		2400					
			OVER		2400					
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